

# Phillips Scientific

## Quad Majority Logic Unit

## NIM MODEL 756

### FEATURES

- \* Versatile Logic Module with Majority Level Selection
- \* Four Independent Channels
- \* Both Overlap and Updating Outputs
- \* 150 MHz Updating Rate and 300 MHz Overlap
- \* Fast Veto for Anti-Coincidence Decisions

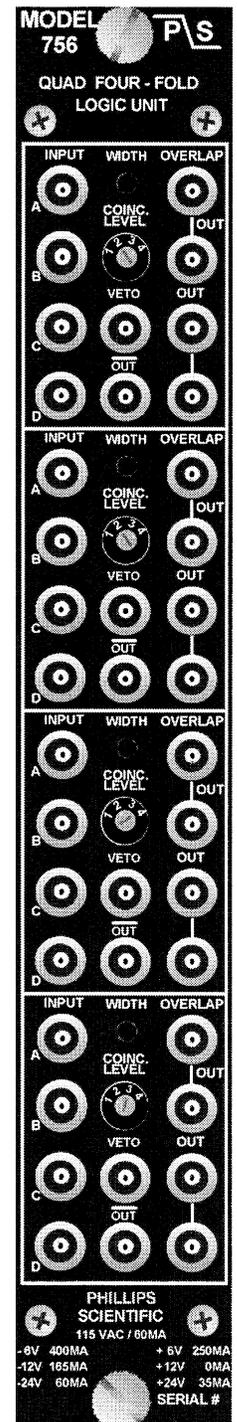
### DESCRIPTION

The model 756 logic unit contains four channels of four input logic with veto in a single width NIM module. Logic AND, OR, Majority logic, Fan-in/Fan-out, and Anti-coincidence functions can be performed with this versatile module. All functions are direct coupled and operate with input overlap times as narrow as 1nSec.

Each channel has four logic inputs, an anti-coincidence input, a coincidence level switch, and five outputs. The inputs are enabled by connecting the input cable to the desired input, eliminating errors often occurring with switched inputs. The setting of the coincidence level switch then determines whether a logic, OR, AND, or Majority logic function will produce an output.

After the inputs have satisfied the logic function desired, triggering of a regenerative stage produces a standardized output pulse for the three updating outputs, variable from 4nSec to 1mSec, independent of the input pulse shapes or overlap times. The two overlap outputs bypass the regenerative stage and produce output widths equal to the input overlap time. This permits outputs up to the maximum input rate capability of 1nSec width and 300 MHz rate.

The outputs are the current source type with two pairs of negative bridged outputs and one complement for each channel. When only one output of a bridged pair is used, a double-amplitude NIM pulse (-32mA) is generated for driving long cables with narrow pulse widths. The outputs have transition times of typically 1nSec, and their shapes are virtually unaffected by the loading conditions of the other outputs.



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## INPUT CHARACTERISTICS

### **A,B,C,D:**

Four (4) LEMO connector inputs per section; Accepts NIM level logic signals (-500mV), 50 ohm input impedance,  $\pm 2\%$ , direct coupled; Input reflections are less than  $\pm 5\%$  for a 1nSec input risetime. Inputs are protected to withstand  $\pm 50$ Volts for 1mSec with no damage. The inputs respond to a 1nSec or longer input pulse width.

### **Fast Veto:**

One LEMO connector input per section; accepts normal NIM level logic signal, (-500mV), 50 ohm input impedance, direct coupled; Protected against damage for  $\pm 50$ Volt input transients. Requires a 3.5nSec minimum input pulse width in time with the input pulse leading edge to inhibit the updating outputs. The overlap outputs will simply be gated for the duration of the veto pulse.

### **Bin Gate:**

Rear panel slide switch enables or disables the slow bin gate via the rear connector. Signal levels are in accordance with the TID-20893 standard. The entire module will inhibit within 10nSec from the bin gate signal.

## GENERAL PERFORMANCE

### **Logic Functions:**

Logical AND, OR, Majority logic and Fan-in/Fan-out. All functions have leading edge inhibit for the updating outputs, and bin gate inhibiting for both updating and overlap outputs.

### **Continuous Repetition Rate:**

Updating - Greater than 150 MHz throughput rate, typically 160 MHz, with output width set at minimum.

Overlap - Greater than 300 MHz throughput rate, typically 325 MHz.

### **Double-Pulse Resolution:**

Updating - Less than 6.5nSec, typically 6.2nSec, with output width set at minimum.

Overlap - Less than 3.3nSec, typically 3.1nSec.

### **Input to Output Delay:**

Updating - Less than 8.5nSec.

Overlap - Less than 5.0nSec.

### **Multiple Pulsing:**

None; One and only one output pulse regardless of input pulse amplitude or duration.

### **Power Supply Requirements:**

- 6 Volts @ 400 mA

+ 6 Volts @ 250 mA

115 VAC @ 60 mA

-12 Volts @ 165 mA

+12 Volts @ 0 mA

-24 Volts @ 60 mA

+24 Volts @ 35 mA

**Note:** All currents are within NIM specification limits permitting a full powered bin to be operated without overloading.

### **Operating Temperature:**

0 °C to 70 °C ambient.

### **Packaging:**

Standard single width NIM module in accordance with TID-20893 and Section ND-524.

### **Quality Control:**

Standard 36-hour, cycled burn-in with switched power cycles.

## OUTPUT CHARACTERISTICS

### **General:**

Five (5) LEMO connector outputs per section; Two pairs of negative bridged outputs and one complemented NIM. The bridged outputs deliver -32mA into a single 50 ohm load (-1.6Volts) and -16mA (-800mV) with both terminated. The complementary output is quiescently -16mA (-800mV) and goes to 0mA during output. The output rise and fall times are less than 1.5nSec from 10% to 90% levels. The output pulse shapes are optimized when the bridged outputs are 50 ohm terminated.

### **Width Control:**

One control per section; 15-turn screwdriver adjustment. Outputs are continuously variable from 4nSec to 1mSec. Width stability is better than  $\pm 0.15\%/^{\circ}\text{C}$  of setting.

### **Updating Output:**

The output pulse will be extended if a new input pulse occurs while the output is active. This provides deadtimeless operation and a 100% duty cycle can be achieved.

### **Overlap Output:**

Output width will equal the input overlap coincidence time. Capable of producing 1nSec or longer output width.